

DISCUSSION ITEMS AND SUMMARY ON PRIORITIES

BUSINESS MODEL

- Need for an open processor IP multi-industrial ecosystem with industrialization options – value chain collaboration
- Covering various market segments from low-end to high-end -> “families” of processors
- **Europractice**-type of service for open-source hardware chips - including standardization ?
- Leverage open-source IC-design – how to accelerate open-source usage & quality assurance ? Legal framework ?
- What does sovereignty mean for soft cores ? [EDA-sovereignty in Europe](#)

SYSTEMS / OVERALL ARCHITECTURES

- Evolution from hierarchical control architectures to flexible orchestration with edge
- Open-source **beyond RISC-V** (additional HW & SW)
- From SW defined HW towards SW driven HW
- **Common validation effort**

HARDWARE

- Scalable performance and modular-based hardware IP ; not only cores, but many IPs needed
- Need for General Purpose Processors + ability to enhance Instruction Set Architecture and modify GPP + availability of accelerators & peripherals
- **Scalable & safety-conform** processor IP, extendable with custom instructions, same-on-chip architectures for different performance classes
- **Safety & Security Certification** (ISO 26262 conformity, DO178, common criteria, CENELEC,...)

SOFTWARE

- Need for OS/RTOS, Hypervisor, software and compilers / development tools
- Safety manual & safety mechanisms for automotive quality – **safety-ready toolchains**