

# **2D Materials for Future Heterogeneous Electronics and the 2D-Experimental Pilot Line**

**Workshop “Promising technologies for ECS”  
September 22<sup>nd</sup>, 2022**

**Max C. Lemme**

**RWTH Aachen University  
AMO GmbH**

# 2D-Materials for Future Heterogeneous Electronics

## The (AI) chip makes the product

Chip hardware as a strategic technology

- Technology Sovereignty (Chips Act)
- Energy Efficiency (Green Deal)
- Reliable Supply Chains
- Cyber Security
- Data security and privacy
- Ethics of AI

**2D PILOT LINE**

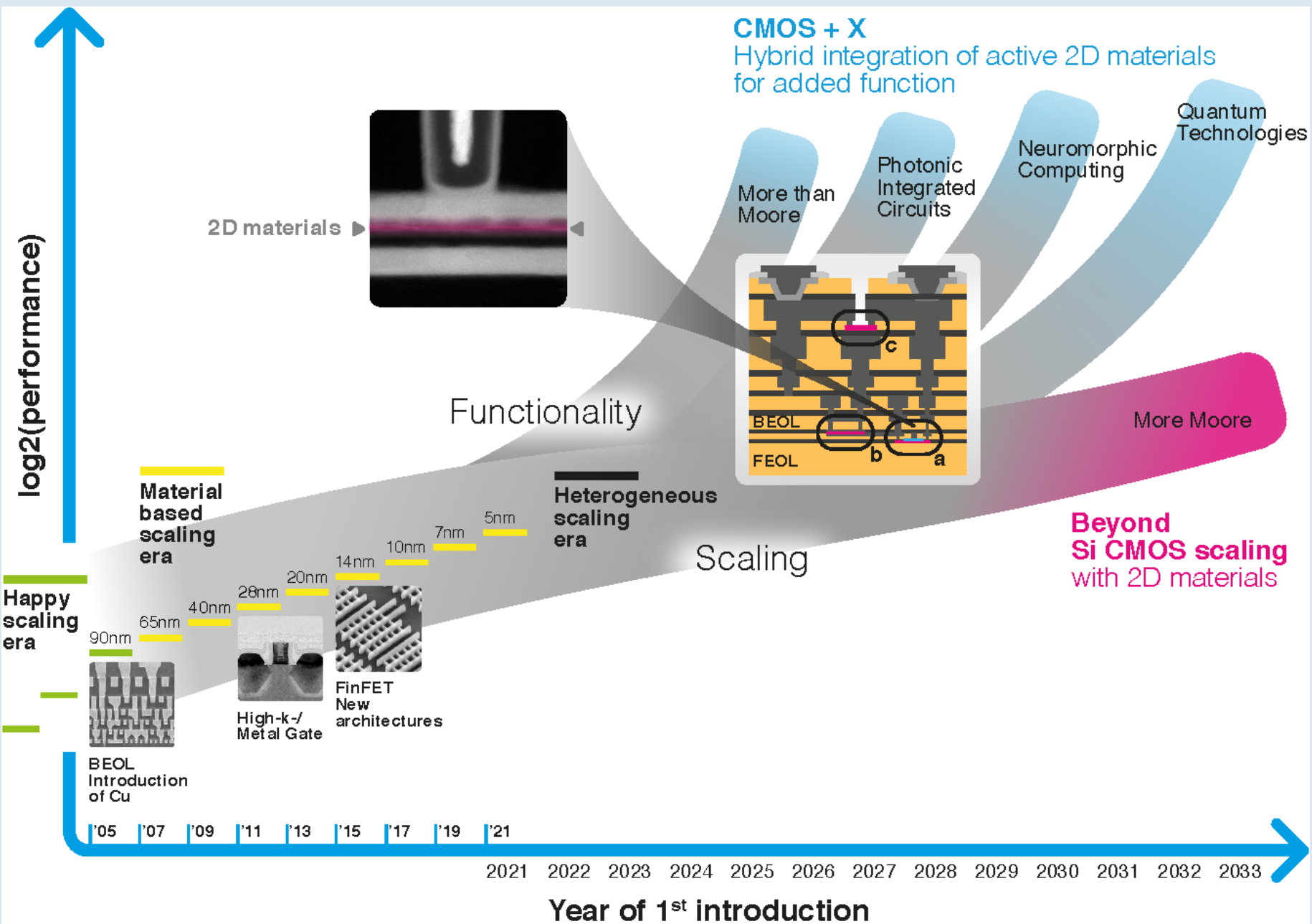


„Cyber-attacks are moving... from software to hardware, threatening devices in homes, cars, businesses, networks, and cloud.“



Funded by  
the European Union

# 2D-Materials for Future Heterogeneous Electronics



Lemme, Akinwande, Huyghebaert, Stampfer

“2D materials for future heterogeneous Electronics”

Nature Communications, 13:1392, 2022

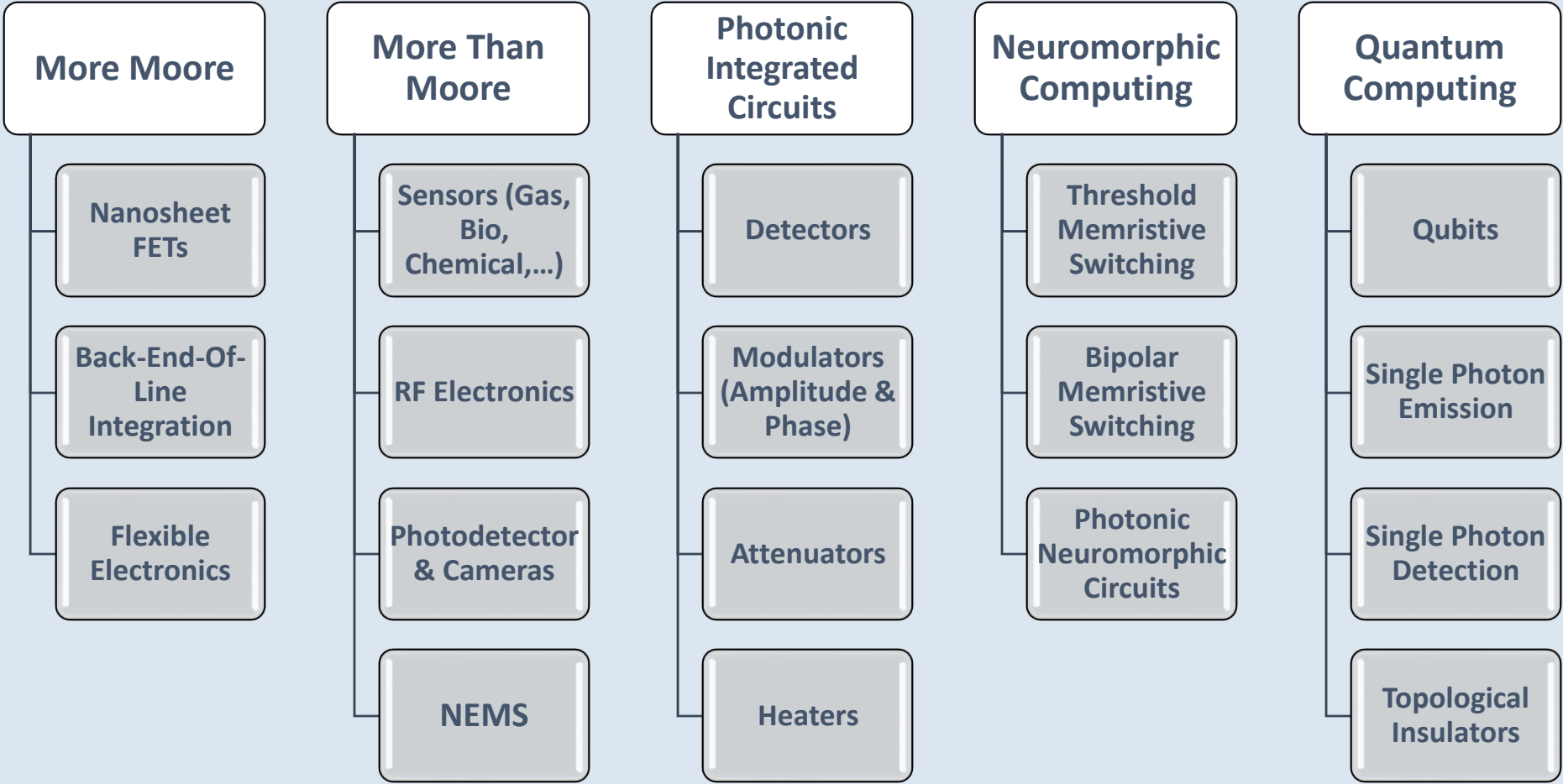


Funded by the European Union

# 2D-Materials for Future Heterogeneous Electronics

Lemme *et al.*, Nat. Comm., 2022

## 2D-Material Integration: Opportunities

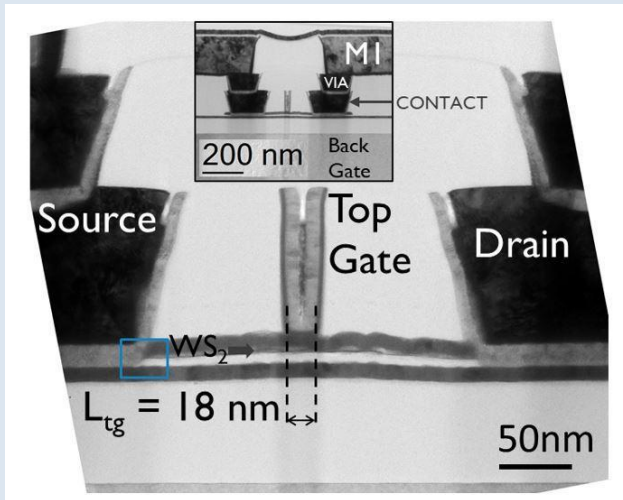




# 2D-Materials for Future Heterogeneous Electronics

## More Moore

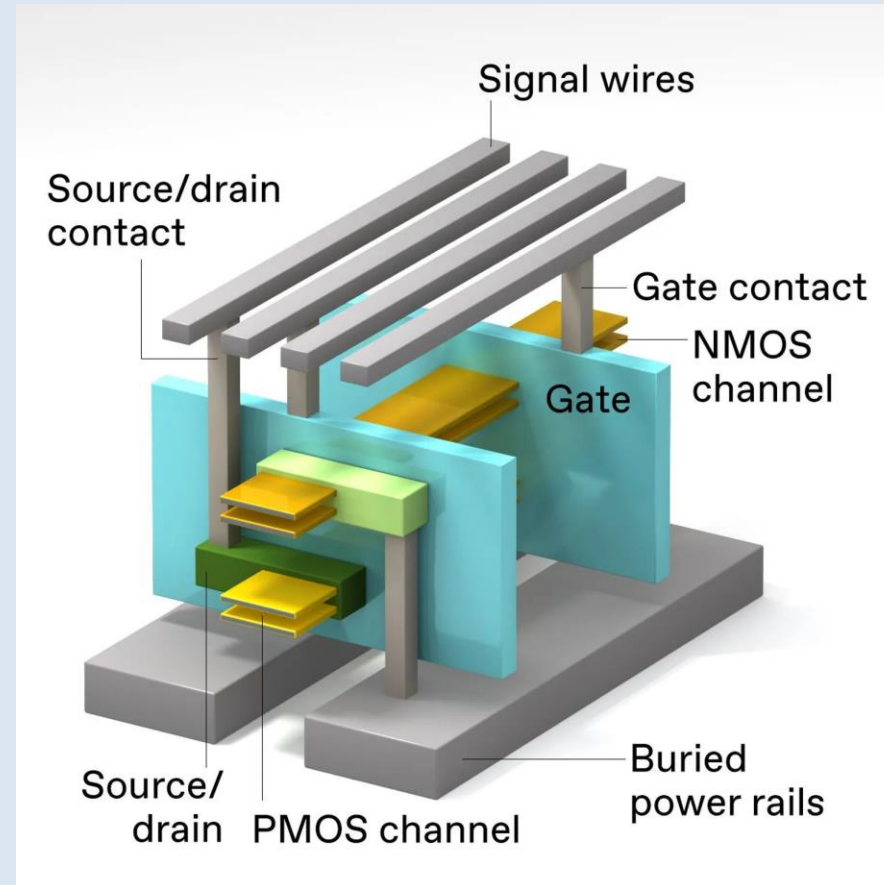
- 2D Nanosheet FETs
  - Ultimate electrostatic control
  - No loss of mobility
  - BEOL integration → 3D



Source: IMEC

<https://www.imec-int.com/en/articles/imec-introduces-2d-materials-logic-device-scaling-roadmap>

**2D PILOT LINE**



Radosavlevic et al., IEEE Spectrum, 2022



2D Materials may be the ultimate channel material for "end of the roadmap" **CMOS scaling**



Funded by  
the European Union

# 2D-Materials for Future Heterogeneous Electronics

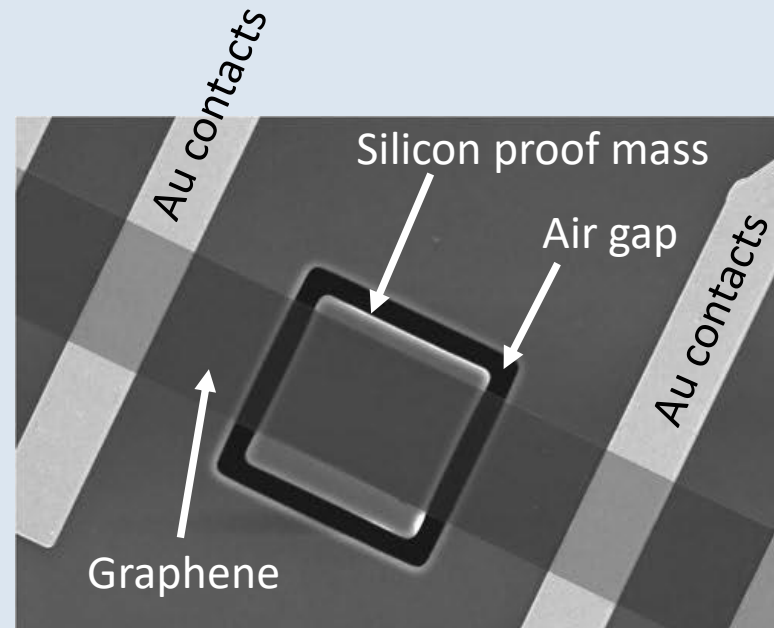
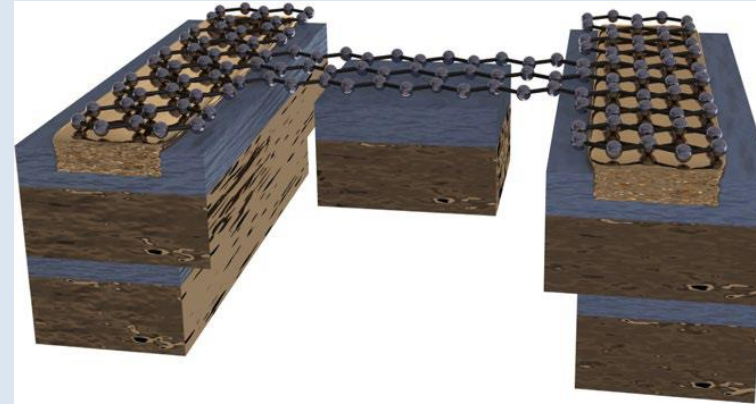
## Graphene NEMS Accelerometer

- Graphene acts as membrane and transducer
- Silicon proof mass attached to graphene membranes (1.000 x reduction compared to SoA)
- Reduced footprint compared to SoA (1/100 x)
- Double layer graphene enhances reliability and yield

Fan *et al.* Nature Electronics, 2, 394-404, 2019

Fan et al. Nano Letters, 19, 6788–6799, 2019

Lemme et al., Research, 8748602, 2020 (review)

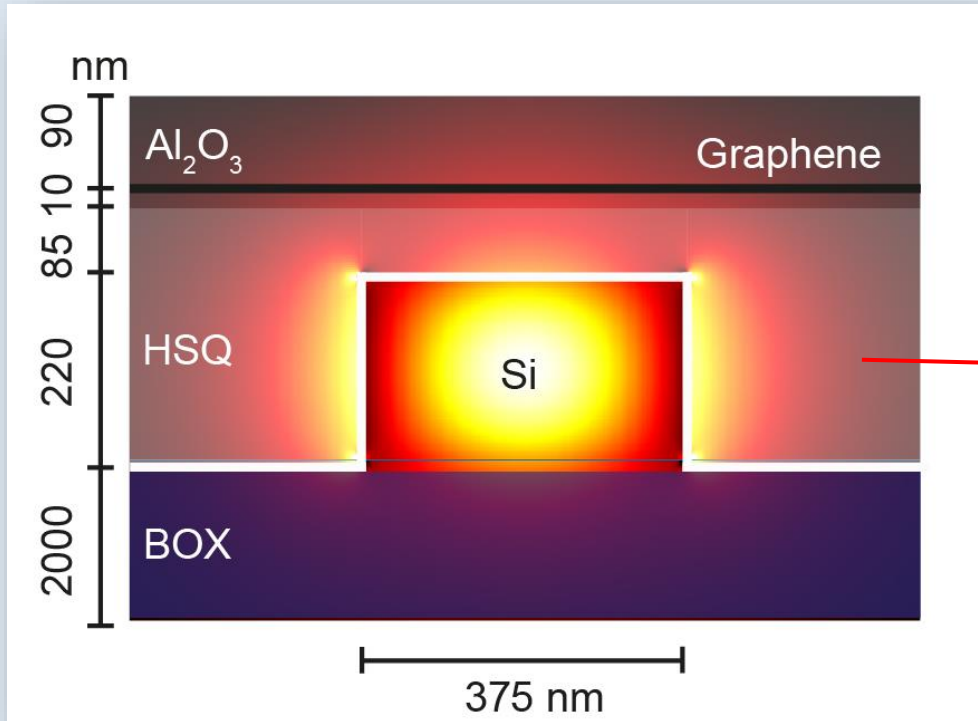


2D materials make superior:

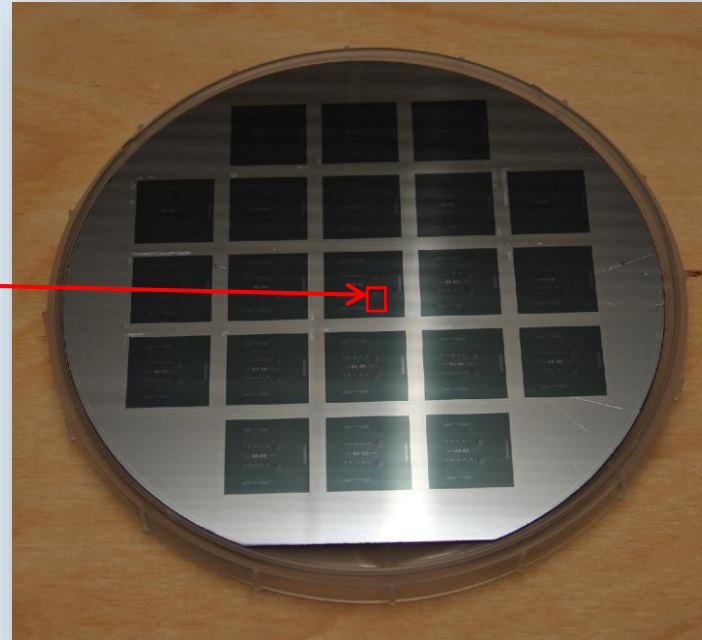
- pressure sensors
- microphones
- accelerometers
- mass sensors
- gas sensors

# 2D-Materials for Future Heterogeneous Electronics

## Graphene / Silicon Photonics Integration



Schall *et al.*, ACS Photonics, 2014



Active silicon  
photonics in the  
back end of the line  
(BEOL)

Graphene photodetector key performance indicators:

- Integrable on various substrates, here silicon (SOI) waveguide
- Possible intrinsic bandwidth: few hundred GHz
- **Ultrafast charge carrier dynamics ► ultrafast photonic devices**

**2D PILOT LINE**

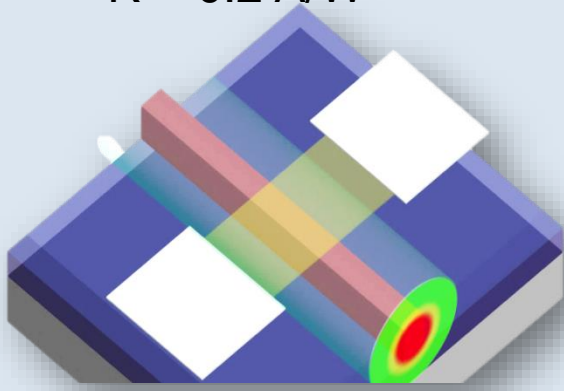


Funded by  
the European Union

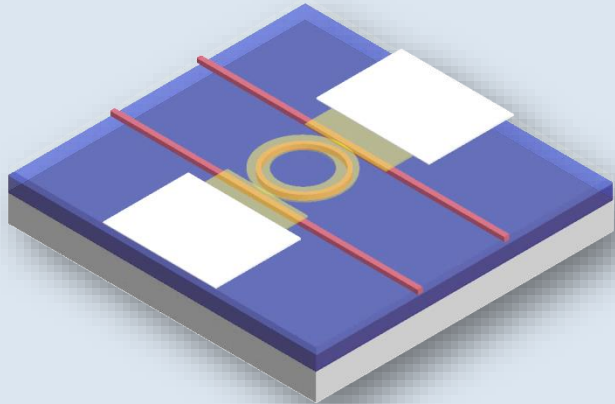
# 2D-Materials for Future Heterogeneous Electronics

## Graphene / Silicon Photonics Integration

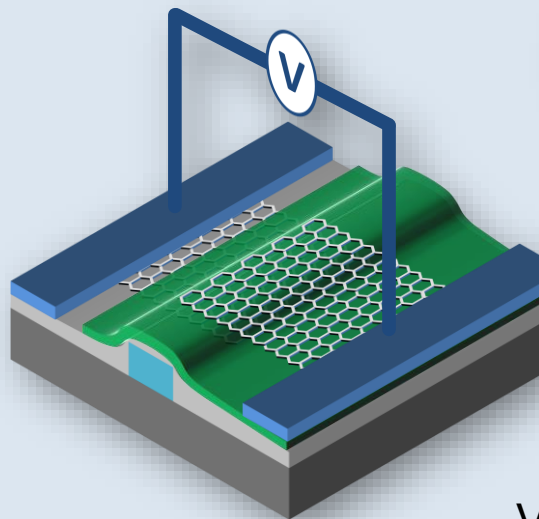
Photodetector: 130 GHz  
 $R = 0.2 \text{ A/W}$



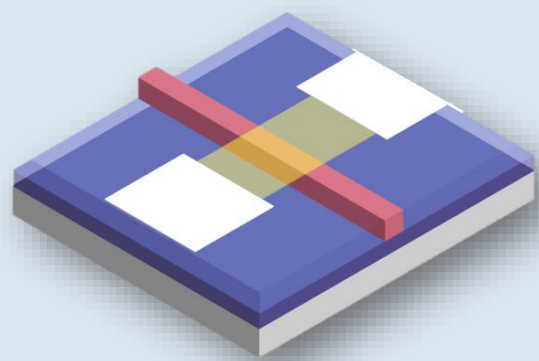
Phase modulator:  $V_{\pi}L\alpha = 14 \text{ dBV}$



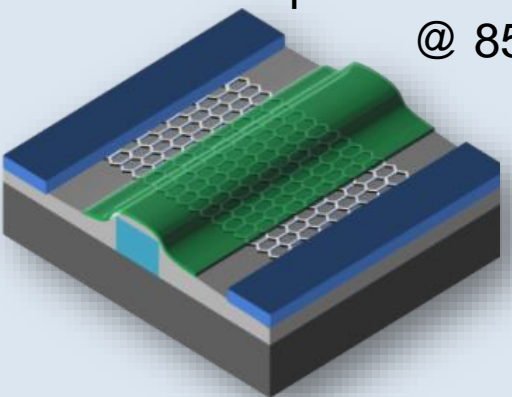
Absorption modulator:  
Extinction 16 dB, IL 3 dB



Heater: 0.3 nm/mW  
rise/fall 3  $\mu\text{s}$



Variable Optical Attenuator  
@ 855 nm



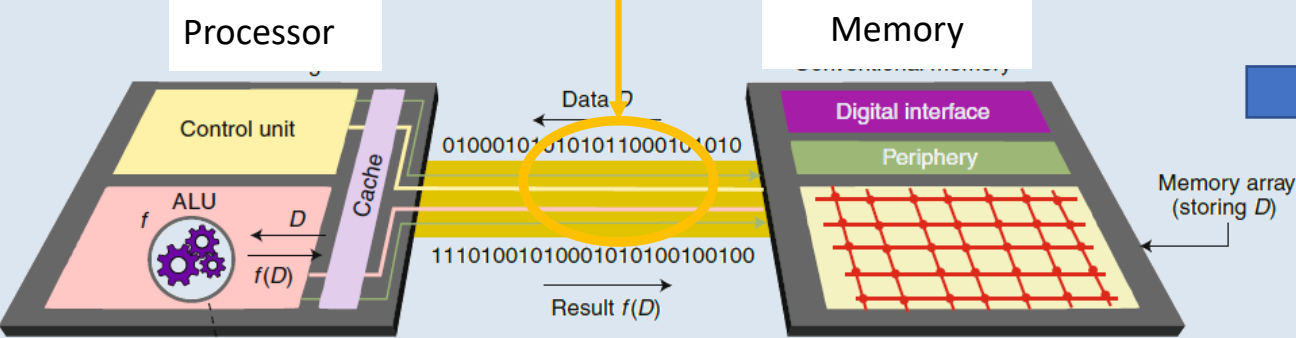
Active **ANY**  
photonics,  
*anywhere*



# 2D-Materials for Future Heterogeneous Electronics

## Machine Learning and Artificial Intelligence: Why new hardware?

### Von-Neumann bottleneck



### Neuromorphic Computing

(2) A. Sebastian et al., Nat. Nanotechnol., 1–16 (2020).

Consumption	CO <sub>2</sub> e (t)
Human life, avg, 1 year	5
US American life, avg, 1 year	16.4
Car, avg incl. fuel, 1 lifetime	57.152
Training one model (GPU)	
NLP pipeline, full tuning	35.592
Transformer (big), full tuning	284.019

Estimated CO<sub>2</sub> emissions  
from training common  
natural language  
processing (NLP) models  
and familiar  
consumption <sup>(1)</sup>

(1) E. Strubell et al., in Proceedings of the 57th Annual Meeting of the Association for Computational Linguistics Italy, 2019 pp. 3645–3650.

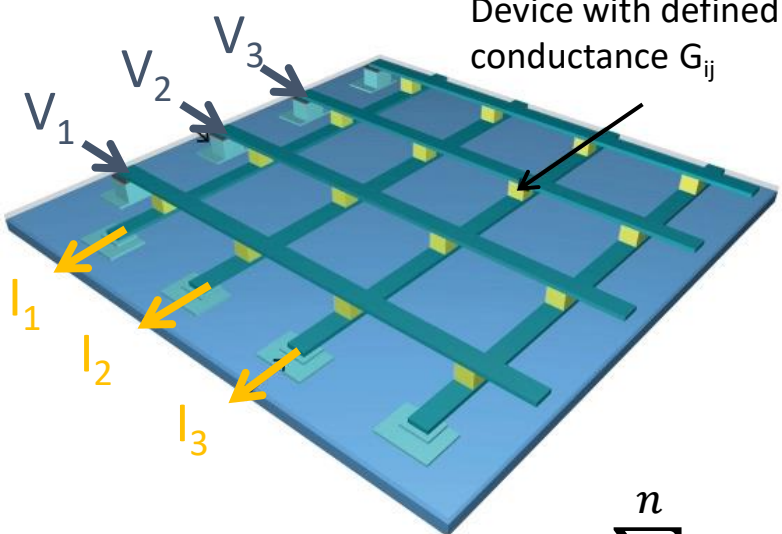


# 2D-Materials for Future Heterogeneous Electronics

## Neuromorphic computing: „Beyond-von-Neumann“-Concepts

### Computing in memory

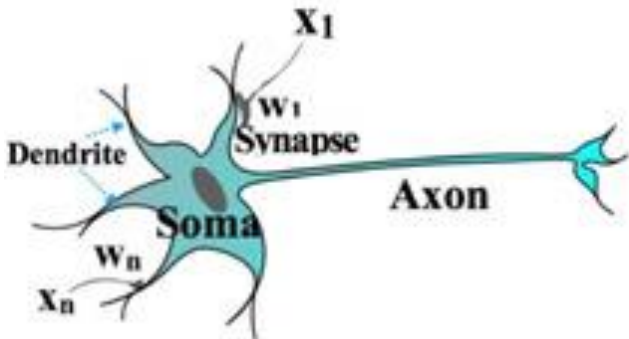
Device with defined conductance  $G_{ij}$



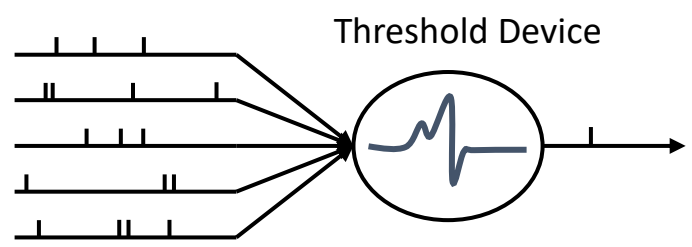
$$\vec{I} = G\vec{V} \Leftrightarrow I_i = \sum_{j=1}^n G_{ij}V_j$$

Adapted from: (3) A. Mehonic et al., Advanced Intelligent Systems. 2, 2000085 (2020).

### Biological Neural Network



### Spiking Neural Network



Adapted from: (4) X. Zhang et al., physica status solidi (a). 215, 1700875 (2018).



2D Materials can be integrated into “memristive devices” that excel at key specifications for **Neuromorphic Computing**

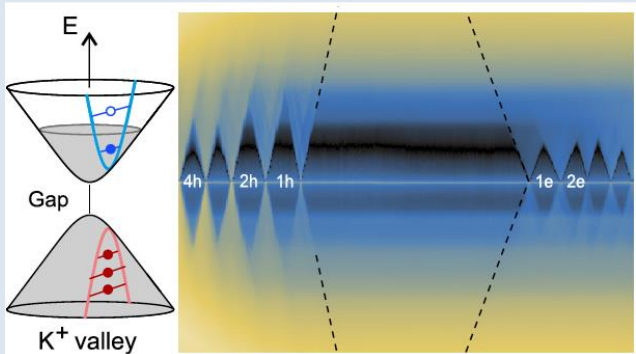
# 2D-Materials for Future Heterogeneous Electronics

## Quantum Computing

Liu and Hersam, Nat. Rev. Mat., 2019

### Qubits

- Valley and spinvalley qubits
- „Valleytronics“



Banszerus, *et al.*, Nano. Lett, 2020

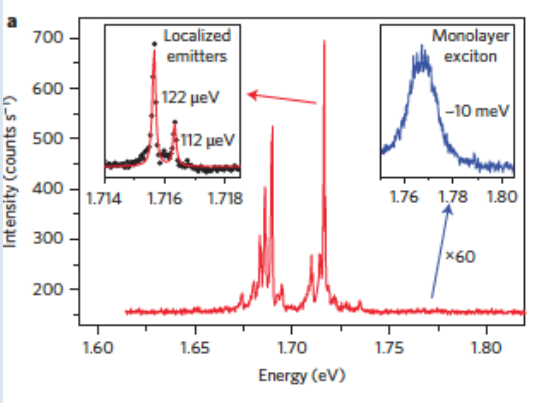


### Topological Insulators

- Moire Heterostructure
- „Twistronics“

### Single photon emitters

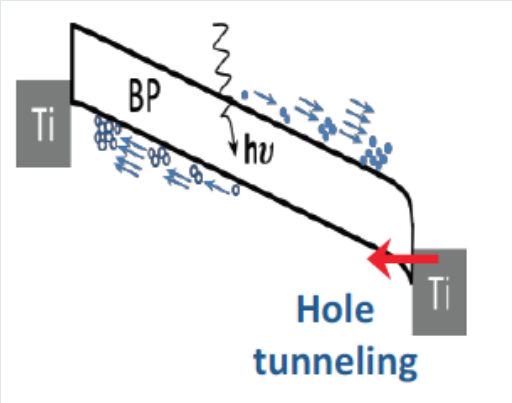
- Si-Photonics integrable



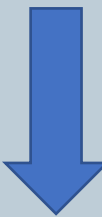
He, *et al.*, Nat. Nano., 2015

### Single photon detectors

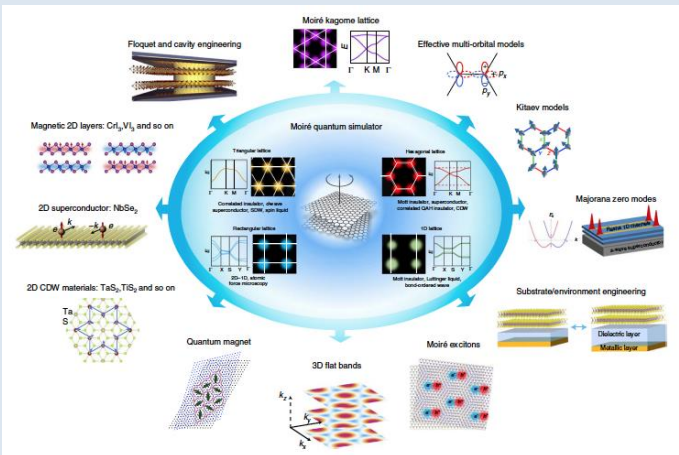
- Avalanche photodetector



Atalla and Koester, DRC, 2017



2D Materials may provide key components for **Quantum Computing**

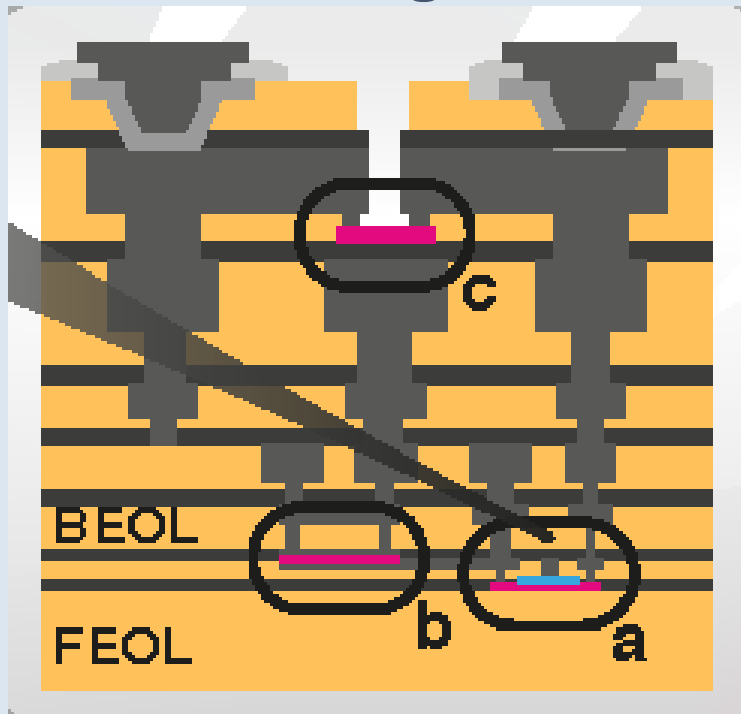


Kennes *et al.*, Nat. Phys, 2021



# 2D-Materials for Future Heterogeneous Electronics

## 2D-CMOS Integration: Challenges



### Growth

- Catalytic CVD on metals
- Temperatures: 400-1000°C
- Quality

### Transfer process

- Quality
- Automation

### Etching

- Etch stop → ALE

### Encapsulation

- ALD vs. 2D

### Electrical contacts

- Graphene
- Semiconducting 2D

Neumaier, et al., Nature Materials, 2019

Akinwande *et al.*, Nature, 2019

Illarionov *et al.*, Nature Communications, 2020

Quellmaltz *et al.*, Nature Communications, 2021

**2D PILOT LINE**



A number of **Engineering Challenges** remain before we see 2D Materials-based electronics / optoelectronics



Addressed by the **2D-EPL**



Funded by  
the European Union



# 2D-Materials for Future Heterogeneous Electronics

## European 2D Experimental Pilot Line

- H2020 project to develop technology (not a specific application)
- Start in 10/2020, 4 years, 20 M€ funding
- Goal: technology transfer to Europractice and European Industry

### 1. Development of tools & materials



### 2. Development of module & platform



### 3. Multi-purpose wafer runs



**2D PILOT LINE**

#### Industrial Advisory Board

X-FAB
AMS
NXP
Infineon
STMicroelectronics
Emberion
Nokia
ELMOS
QERV

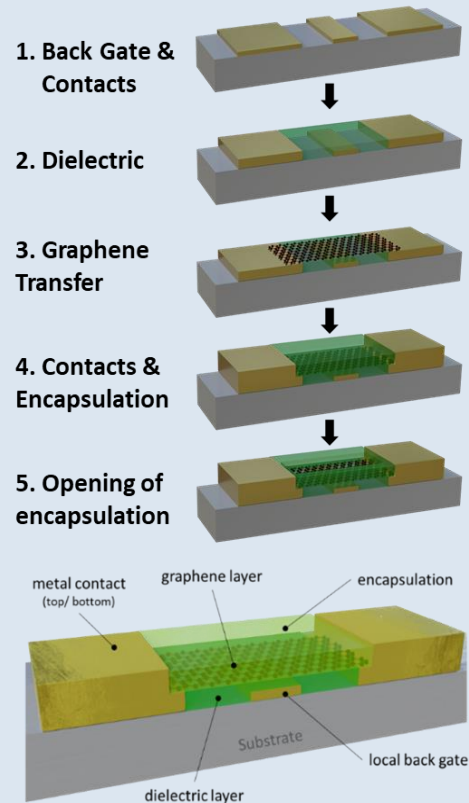


Funded by  
the European Union

# 2D-Materials for Future Heterogeneous Electronics

- MPW run #1 - In the fabrication line
- MPW run #2 - Open for Application

## Graphene FETs for sensor application



Multi Project Wafer Run 1 Application

Name \*

Email \*

Address \*

Full address with city and postal code

Phone number \*

Include country code

Country \*

Company/Institution \*

Institution Type

☐ Academic

☐ Research institution

☐ Start-up

☐ SME

☐ Large business

☐ Other

Sector

☐ Government Research

☐ Academic

☐ Other

Affiliation to Graphene Pilot Line

☐ I am affiliated with the Graphene Pilot Line

☐ I am not affiliated, my institution is in Europe

☐ I am not affiliated, my institution is not in Europe

I am \*

☐ Interested in being contacted with more information about this MPW run.

☐ Interested in participating in this MPW run.

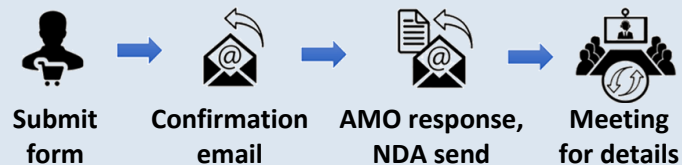
Message

Add any additional notes or questions here.

Consent for storing submitted data \*

☐ Yes, I give permission to store and process my data. (See terms below.)

Submit



**Important Dates**  
**1 Feb. 2022:**  
Call opens for applications

**30 June 2022:**  
Call closes

**1 Sept.– 31 Oct. 2022:**  
**MPW run**

